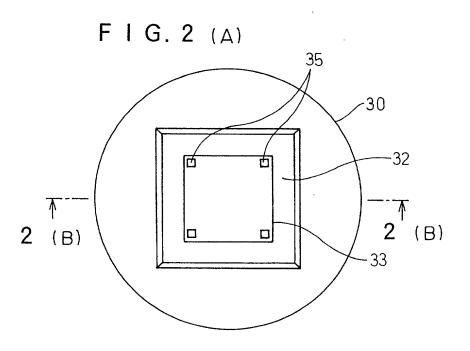
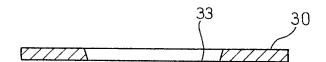
SIGNAL PROCESS-ING CIRCUIT 92 COMPUTER 75 DIGITAL ARITHMETIC CIRCUIT CONDENSER LENS POWER SOURCE MAIN DAC/AMP SUBSIDIARY DAC/AMP 738 ۲48 74, 8 1,66 9 \sim 15 - 50 50 38 doo 36~ $_{\infty}$

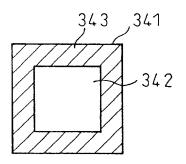
F | G. 1



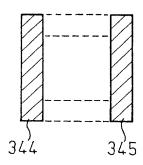
F I G. 2 (B)



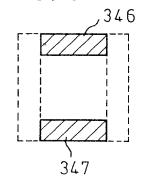
F I G. 3 (A)

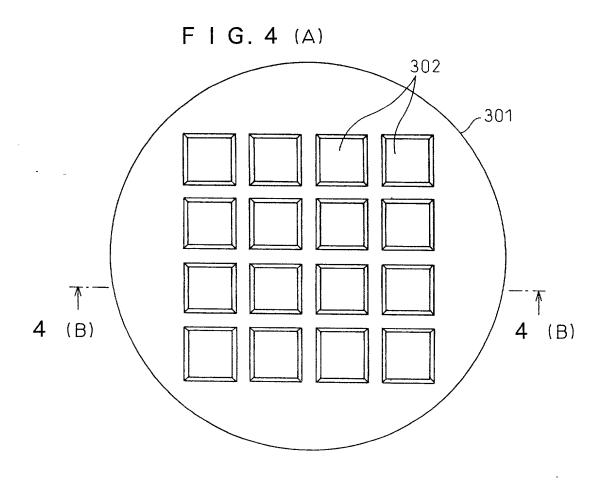


F I G. 3 (B)

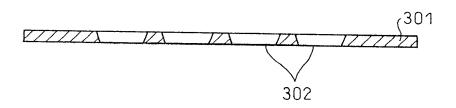


F I G. 3 (C)

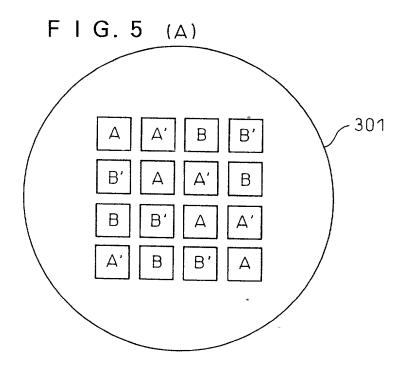


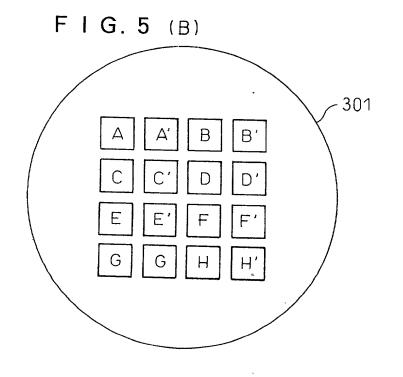


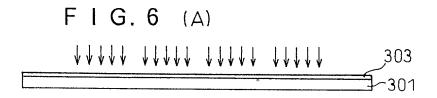
F I G. 4 (B)

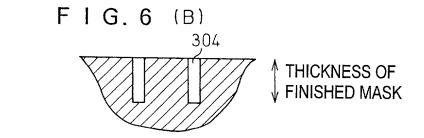


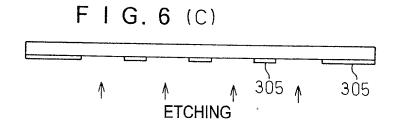
1.450 9.50

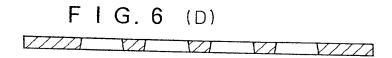












F I G. 7

